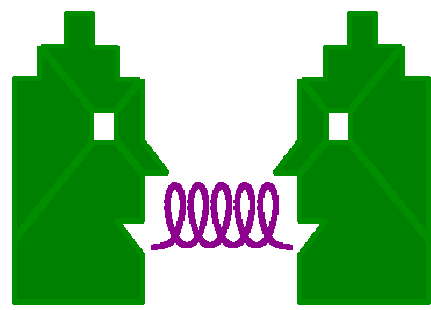




Silicon Detectors @ the LHC



ICHEP02

Chris Parkes

ALICE	0.3 + 1.3 + 2.9 m ²	Pixels+Drift+Strips
LHCb	0.3	Strips (L + Strips (RICH))
ATLAS	2.2 + 0.1 m ²	Pixels + Strips
CMS	0.8 + 206 m ²	Pixels + Strips

TOTAL = 287 m²
= 3.6 secs/m²

Contents

- Layouts

- x 4

Then pick a few examples....

- Sensor Designs

- ALICE Si Drift Detector, LHCb VELO

- CMS Strip Optimisation

- FE Electronics / Mechanics / Cooling

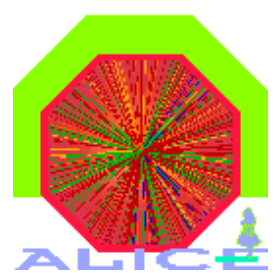
- ATLAS Pixels

- Module Assembly

- CMS Strips

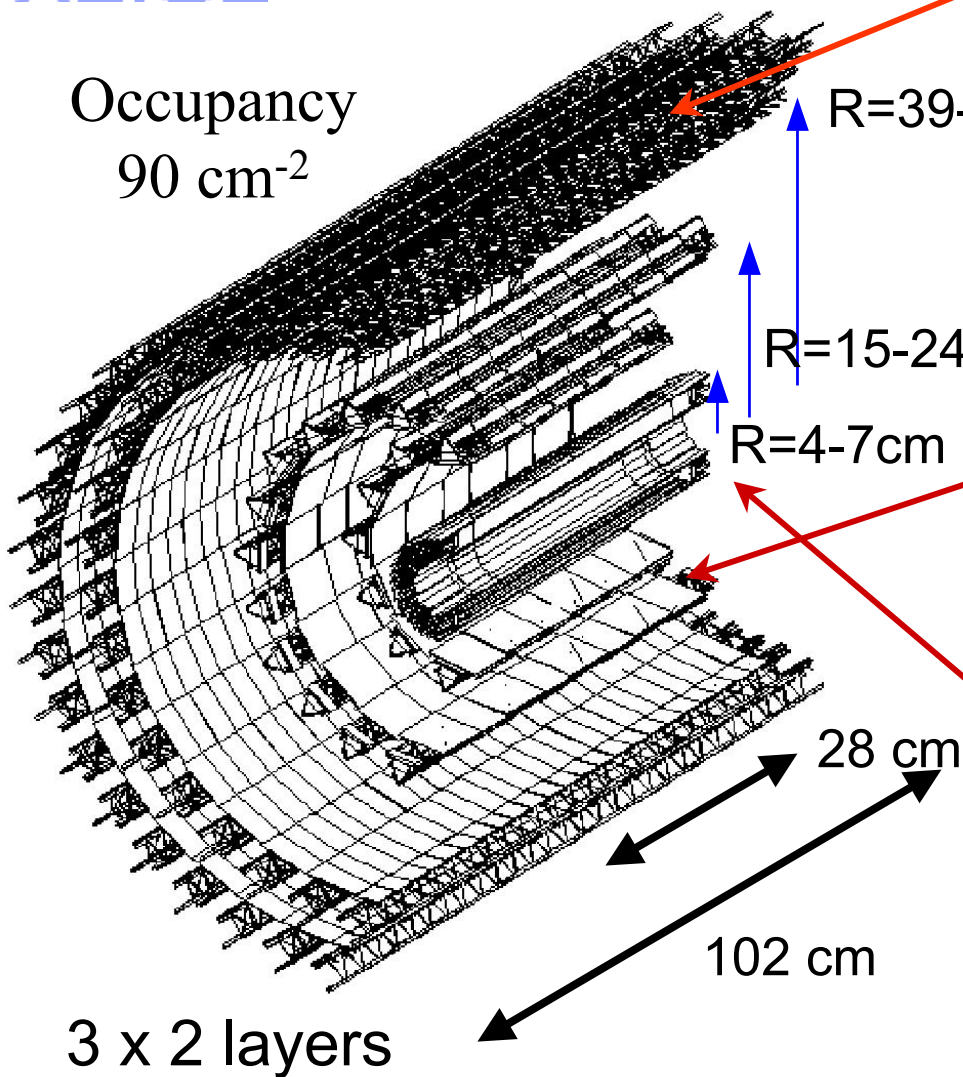
- Q/A

- ATLAS Strips



ALICE Silicon

Occupancy
 90 cm^{-2}



Double-sided Silicon Strips

2.7M $95 \mu\text{m}$ pitch
stereo twist p^+/n^+ on n
strips, analogue rad-tol
 $0.25 \mu\text{m}$ CMOS readout,
1770 modules

Silicon Drift Detectors

260 modules,
 0.13M channels
 p^+/n^+ on n – see later

Silicon Pixel Detectors

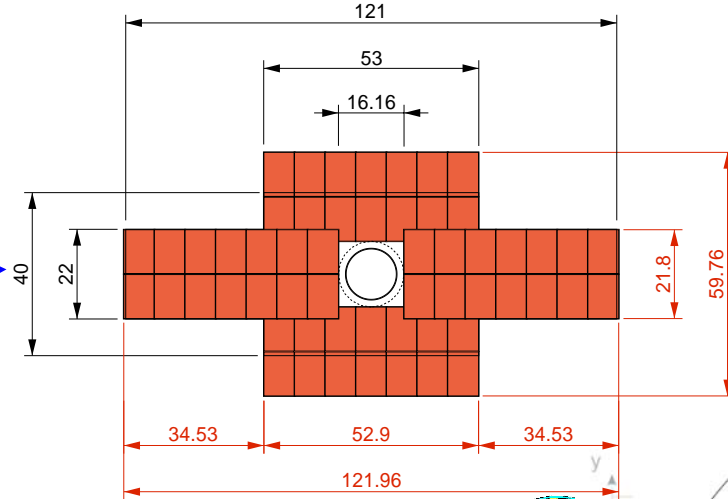
9.8M $50 \times 425 \mu\text{m}$
 p^+ on n pixels
240 modules

3 x 2 layers



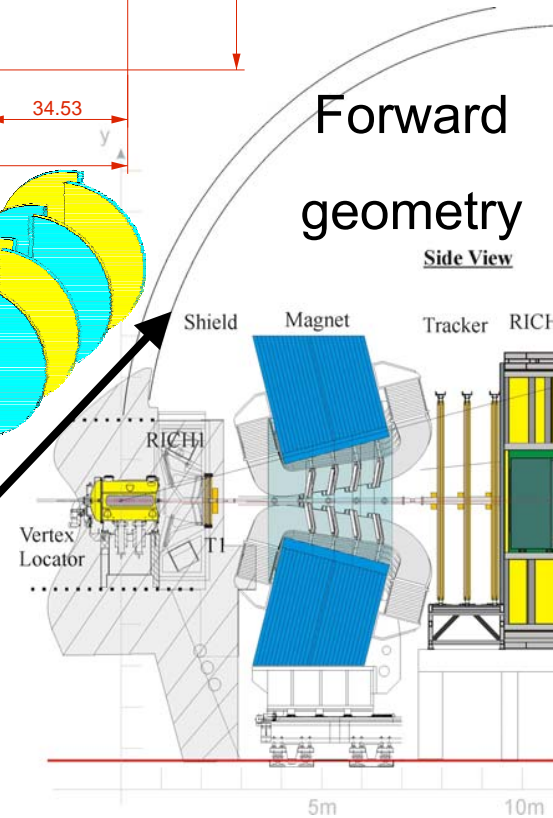
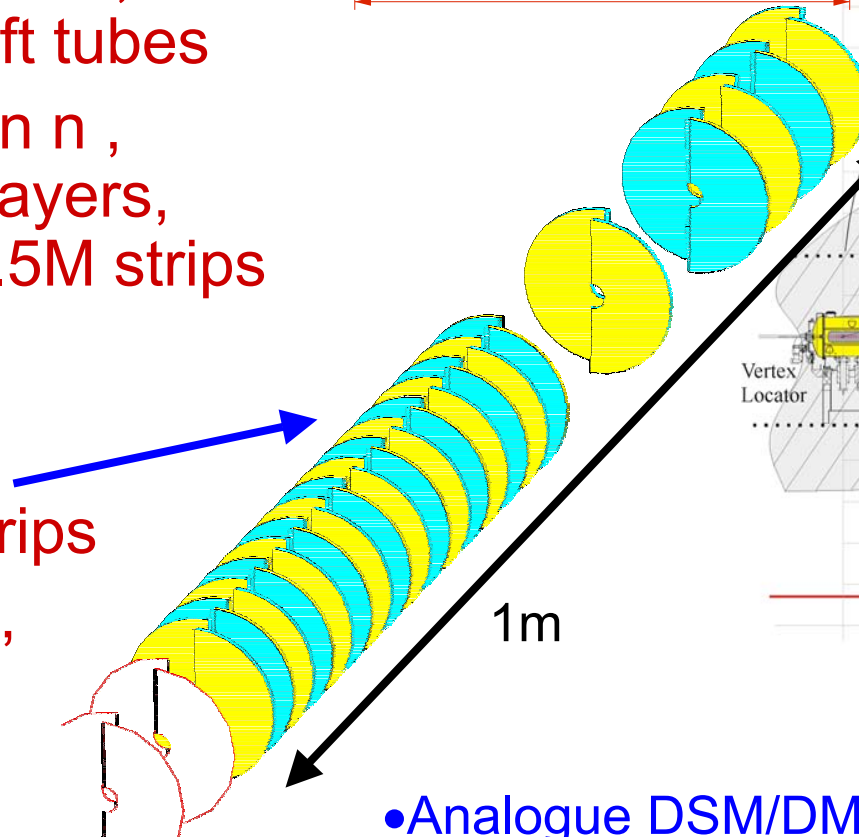
Tracker

- 4 all Si layers +
- Strips close to beam, outside straw drift tubes
- $200\mu\text{m}$ pitch p⁺ on n, stereo twist, 16 layers, 2300 sensors, 0.5M strips



Vertex Locator

- n⁺ on n, 0.2M strips
- 8mm from beam, weird layout!!!
- see later



• Analogue DSM/DMILL readout

ATLAS Inner Detector

Pixels n⁺ on n, 50x400 μm²

3 layers, 8 disks, 1500 modules

r = 4.8 - 16 cm, 140 M channels,

SemiConductor Tracker (SCT)

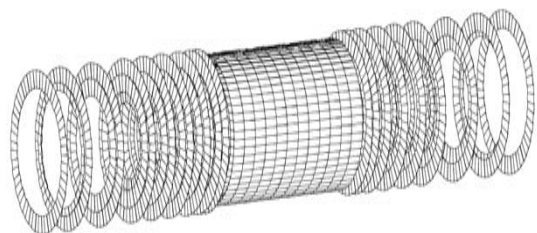
19,300 single sided 80 μm pitch

p-on-n strip detectors

6 sensor geometries

Dmill BiCMOS Binary readout

r = 27 - 52 cm, 6.3 M channels



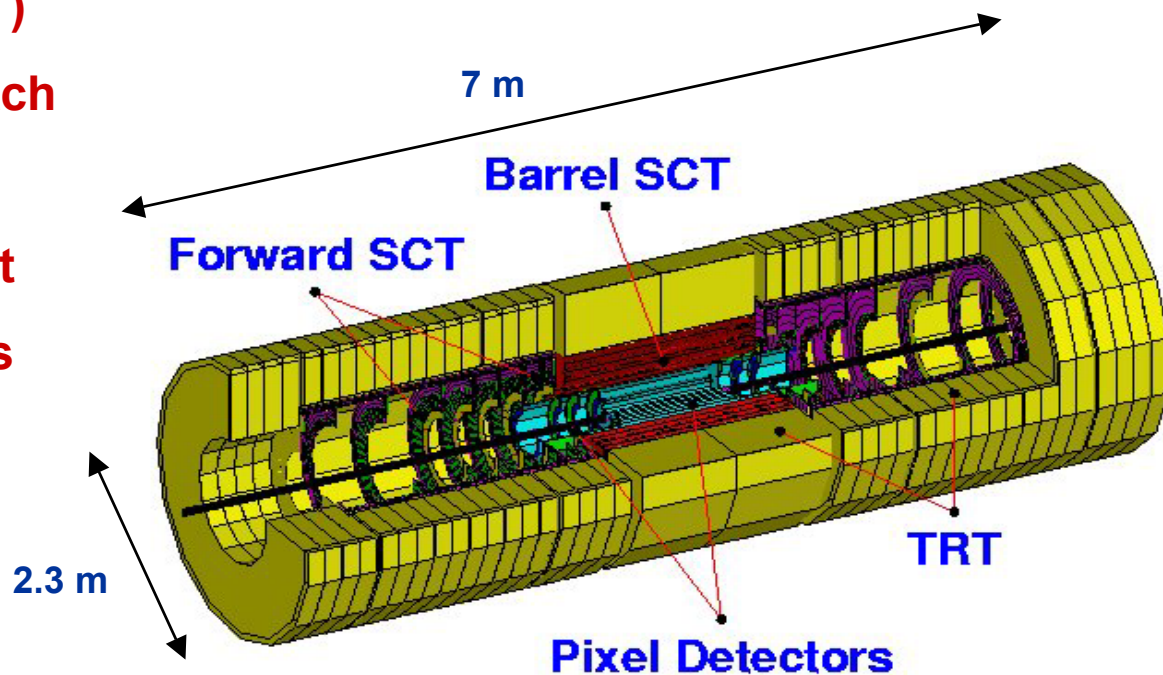
4 Barrel cylinders (square),

2 x 9 End-cap disks (wedge)

Tracking and Vertex reconstruction

Axial magnetic field 2T

Enclosed in a thermal envelope inside
the solenoid, -7°C

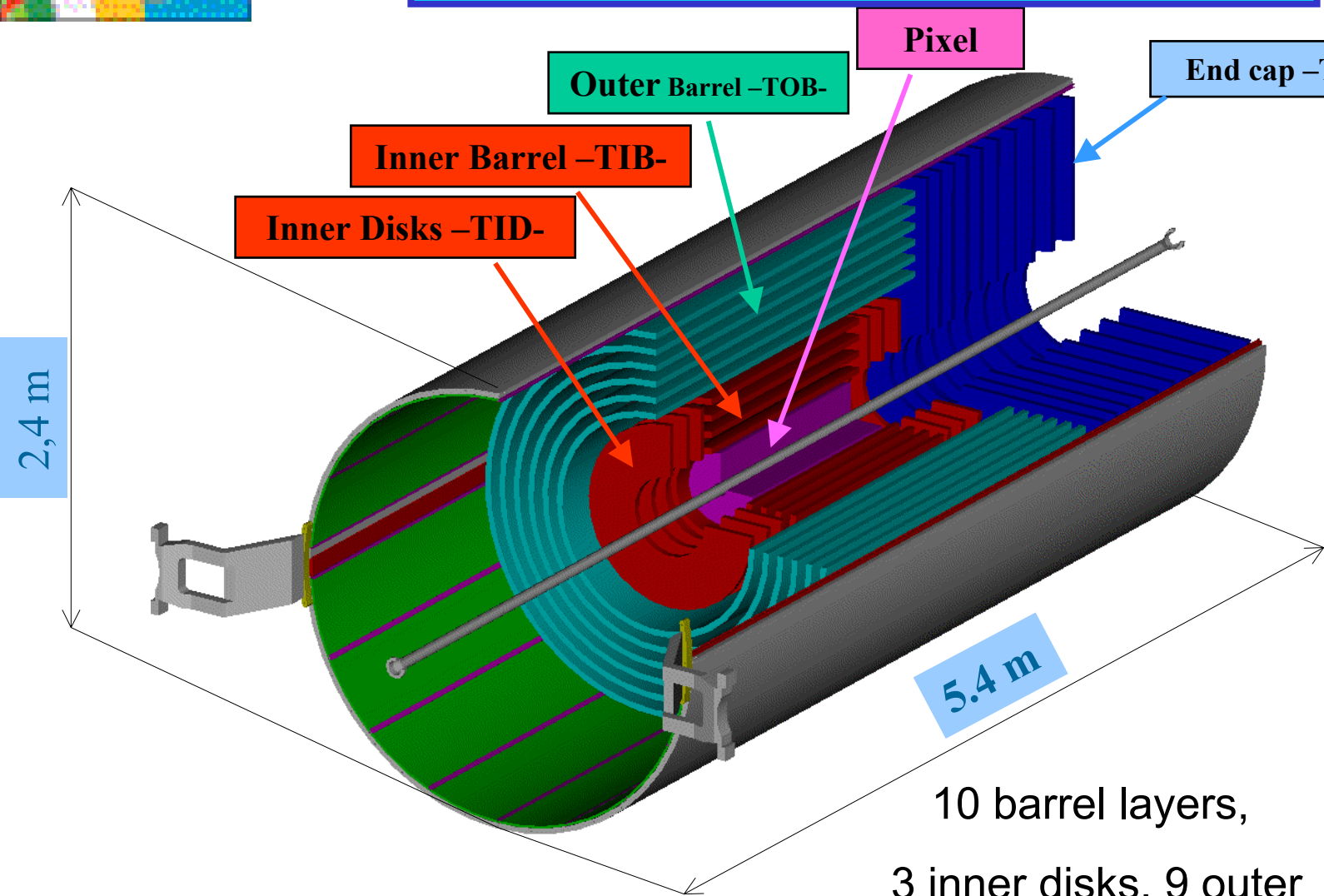




Two key differences of ATLAS/CMS strips

Size: 4,000 → 15,000 modules

Strip readout: Analogue DSM chip - APV



2 Pixel Layers

R=4cm layer to be replaced by R=11cm after low lumi.

10 barrel layers,

3 inner disks, 9 outer